

Notice of Allowability

Application No.

10/007,245

Examiner

Krista M. Flanagan

Applicant(s)

KORGER ET AL.

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Amendment filed on 28 April 2005.
2. ☒ The allowed claim(s) is/are 1-13 and 15-22.
3. ☒ The drawings filed on 28 April 2005 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

Drawings

1. In view of the amendment filed on 28 April 2005, the Examiner withdraws objections to the drawings from the previous Office Action.

Specification

2. In view of the amendment filed on 28 April 2005, the Examiner withdraws objections to the specification from the previous Office Action.

Allowable Subject Matter

3. Claims 1-13 and 15-22 are allowed.
4. The following is an examiner's statement of reasons for allowance:
5. Regarding claim 1, prior art fails to disclose an apparatus with a data ready indicator between different clock domains including a first circuit configured to present a first data signal and a first indicator signal where the first circuit comprises a register circuit configured to complement the first indicator signal in response to a gated version of the first clock signal.
6. Regarding claim 15, prior art fails to disclose a method of indicating data synchronized from a first logic domain to a second logic domain where presenting the first data and indicator signal is done in response to a gated clock signal and where the indicator signal is complement in response to the gated clock signal.
7. Regarding claim 22, prior art fails to disclose an apparatus comprising: a first circuit configured to present a first data signal and a first indicator signal in response to a first clock signal and an enable signal; and a second circuit configured to present a second data signal and a second indicator signal in response to said first data signal, said first indicator signal and a

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second clock signal, wherein said second circuit comprises a first register circuit configured to generate said second data signal in response to said second clock signal and said first data signal, a second register circuit configured to generate a first intermediate indicator signal in response to said second clock signal and said first indicator signal, a third register circuit configured to generate a second intermediate indicator signal in response to said second clock signal and said first intermediate indicator signal and a logic gate configured to generate said second indicator signal in response to said first and second intermediate indicator signals.

8. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion


9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Krista M. Flanagan whose telephone number is (571) 272-2203. The examiner can normally be reached on Monday - Friday, 8 - 4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert J. Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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20050722



**MOHAMMED GHAYOUR
SUPERVISORY PATENT EXAMINER**